

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Duck-Kyon CHOI

Group Art Unit: 2823

Serial No.: 09/170,625

Examiner: S. Hawranek

Filed: October 13, 1998

For: METHOD FOR FABRICATING A THIN FILM TRANSISTOR



RELATED CASES STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant hereby provides a list of pending U.S. applications or issued patents which may be related to the present application.

<u>Examiner's Initial</u>	<u>U.S. Serial Number</u>	<u>Filing Date</u>	<u>Inventor Name</u>	<u>Our Ref.</u>
	09/115,498	04/14/98	JANG et al.	8733.6584
	09/299,571	04/27/99	JANG et al.	8733.8205
	09/350,816	04/09/99	JANG et al.	8733.20020

Date Considered:

FOURSON
6/2/04

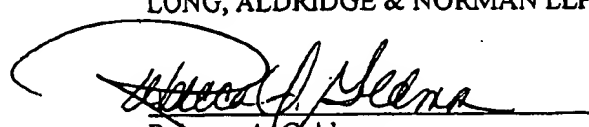
ⓧ see 32 CFR 1.198(a)(2)(iii)
+ 37 CFR 1.198(a)(3)(i)

Respectfully submitted,

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October 5, 2000

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